



ORIGINAL PATENTS
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SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION PTO-1449			ATTY. DOCKET NO. 19329-706		SERIAL NO. 10/037,844		
			APPLICANT Kuang-Chien Chen, et al.				
			FILING DATE 10/19/01		GROUP Not Yet Assigned		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
NL	D. Stoffel, et al., "Logic Equivalence Checking by Optimization Techniques", Proc. Int'l. Workshop on CAD, Test and Evaluation for Dependability, July 1996, pp. 85-90						
NL	D. Brand, "Verification of Large Synthesized Designs", Proc. 1993 IEEE Int'l. Conf. on CAD, November 1993, pp. 534-537						
EXAMINER <i>Nau</i>			DATE CONSIDERED <i>11/06/02</i>				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.